



### **Peripheral Devices**

 Complement the computer to create a computer system

### Three types:

- Input: transfers information from outside to the computer
- Output: transfers information from the computer to the outside
- Input/Output (I/O): bi-directional data transfer

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Memory Unit Data

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CPU











Radix Re	presento	ation: r =	= 10
3	2	1	0
3	1	2	5
10 <sup>3</sup>	10 <sup>2</sup>	10 <sup>1</sup>	10 <sup>0</sup>
1000	100	10	1
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Ro	adix Rej	oresento	ation: r =	= 3	
	3	2	1	0	
	1	0	2	1	
	<b>3</b> <sup>3</sup>	<b>3</b> <sup>2</sup>	3 <sup>1</sup>	<b>3</b> <sup>0</sup>	
	27	9	3	1	
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Bin	ary System		
□ Pre □ Ba □ Ea □ I	evalently used in comput sed on <b>radix two,</b> the sy ch <b>bit can have r = 2 v</b> Either 0 or 1	rers ymbols are called <b>bits</b> alues	
	Decimal System	Binary System	
	1	1	
	2	10	
	3	11	
	4	100	
	5	101	
	10	1010	
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## Scales of Magnitude



 In 2005, a standard was defined for quantities expressed as powers of 2

 Precise numbers for sizes can be stated using multiples of powers of 2, in steps of exponents that are multiples of 10

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Units	Actual Size (bytes)	Other Names
Megabyte (MB)	1,000,000	Million , 10 <sup>6</sup>
Mebibyte (MiB)	1,048,576	<b>2</b> <sup>20</sup>
Gigabyte (GB)	1,000,000,000	Billion, 10 <sup>9</sup>
Gibibyte (GiB)	1,073,741,824	<b>2</b> <sup>30</sup>
Terabyte (TB)	1,000,000,000,000	Trillion, 10 <sup>12</sup>
Tebibyte (TiB)	1,099,511,627,776	<b>2</b> <sup>40</sup>
cales of Mc	agnitudes	
able 2.1: Unit	Names and The	ir Sizes
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### Instruction Execution

- Instructions are executed in timed phases
- Phases may be overlapped
- Each instruction is controlled by a timing mechanism, called a clock
  - Clocks operate according to a **periodicity**
  - This periodicity defines the time step of the phases
  - Some instructions require multiple time steps

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Units	Fraction of a Second	Symbol
Second	1	sec
Hertz	1	Hz
Microsecond	10-6	µsec
Megahertz	106	MHz
Nanosecond	10-9	nsec
Gigahertz	10 <sup>9</sup>	GHz

### Instruction Execution

Most 2013 computers operate on a clock that is between 2 GHz and 10 THz (Table 2.2 : Scales of Time and Frequency) (c) 2012 Ophir Frieder at al

### Instruction Execution



# Actual computer speeds are measured by **instruction rate**

- Expressed in:
  - **MIPS** (Million Instructions Per Second).
  - **GIPS** (Billion Instructions Per Second).
  - **FLOPS** (Floating Point Operations Per Second).
  - TREPS (Trillion "Edges" Per Second new 2011 measure)

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### **Bit Sequences**

- The interpretation of a bit sequence's meaning depends on the usage of the sequence
- A bit sequence can be interpreted as an instruction (numerical, logical, control or program flow)
- Bit sequences can also represent data (integer, float or real, character)

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Character	
One to four bytes	-
Can come as a single	
character or as a part	
ot a string of characters	
The character "9" is	
stored differently than	
the integer Q	



- Programming languages express algorithms using sequences of statements
  - Cannot be executed by a computer because they are not written in binary digits
- Two common methods that computers use to execute programming languages' instructions: compilation and interpretation

Mix – partial compilation to an intermediate code

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### Compilation



- A compiler is a program that performs compilations
  Compilation is a process which
  - **analyzes** and **translates** a program into computer instructions
    - Translates programs statement by statement
  - Does not execute code
  - Stores executable code expressed by bits in a file

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### Interpretation



- Interpretation is a process that analyzes and executes each statement as it is encountered
- Does not produce instruction for the computer
- Two main ways to do interpretation: interactive mode and batch mode

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### Summary

- A computer system includes a computer and peripheral devices of various types
  - Peripheral devices are divided into user and computer interface, communication, and mass memory devices
- A computer consists of a single or several Central Processing Unit(s)
- The von Neumann model of computing is the prevalent model for the architecture of computers

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### Summary

- The memory stores its contents in a sequence of numerical digits expressed in binary.
- The computer uses strings of numbers, or addresses, to access contents in the memory
- Instructions are executed in time phases and controlled by a clock (or several colcks)
- All data are stored in **binary form**, but their interpretation depends on their usage
- Programs are either compiled or interpreted

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